ISTQB
CT-AuT
ISTQB Certified Tester Automotive Software Tester (CT-AuT)

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Question: 1

Imagine you are participating in an Automotive SPICE® – Assessment in your role as integration tester and you are receiving the information that your process has been assessed as "L," using the process attribute PA 1.1.
Which ONE of the following options is correct?
Response:

A. "L" not fulfilled
B. "L" partly fulfilled
C. "L" largely fulfilled
D. "L" fully fulfilled

Answer: C

Question: 2

Which of the following is a dimension defined in Automotive SPICE®?
Response:

A. Process dimension
B. Time dimension
C. Resource dimension
D. Objective dimension

Answer: A

Question: 3

Which interfaces are used to collect and distribute information in an electronic control unit (ECU)?
Response:

A. Environment model, bus system and diagnosis interface
B. Analogue and digital inputs, watchdog and internal data memory
C. Analogue and digital inputs, supply voltage and diagnosis interface
D. Analogue and digital inputs, bus system and diagnosis interface

Answer: D

Question: 4

With which of the measures listed below can the objectives of an increasingly complex software development project be best achieved in the short run?
Response:

A. By insourcing an outsourced projects
B. By using effective methods and processes
C. By ensuring efficient qualification of employees
D. By outsourcing of complex projects

Answer: B

Question: 5

Which items are part of an automotive specific test environment?
Response:

A. Control computer, simulation software, data logger
B. Real-time capable computer, network accesses, report database
C. Measuring devices, specification documents, laboratory
D. Data management tool, operating system, environment model

Answer: A

Question: 6

Which of the following statements is NOT a description of a fault injection test?
Response:

A. Fault injection tests insert faults in the behavior of external components to detect that the system can deal with erroneous situations
B. Fault injection tests insert faults in internal interfaces, e.g. as lost messages
C. Fault injection tests insert faults in the system specification, e.g. as too low parameters for the required performance
D. Fault injection tests insert faults in the operating unit that show as internal defects

Answer: C

Question: 7

What is especially important in the selection of test design techniques in the context of ISO 26262?
Response:

A. White-box-test design techniques should be preferred over black-box-test design techniques, as the tester can take advantage of knowledge of the code
B. The recommendation of the ISO 26262 for the identified ASIL is the decisive factor for the selection of the test design techniques
C. Intuitive test design techniques should always be preferred over structure based test design techniques
D. The combination of the suitability of the test basis and the test level together with a high risk of non-detected errors is the decisive factor for the test design techniques to be selected

Answer: D

Question: 8

What are the six stages in the system product life cycle according to ISO/IEC 24748?
Response:

A. Concept, Development, Acceptance, Utilization, Support, Retirement
B. Concept, Development, Production, Release, Support, Retirement
C. Concept, Implementation, Production, Utilization, Support, Retirement
D. Concept, Development, Production, Utilization, Support, Retirement

Answer: D

Question: 9

Which test is typically performed at a Component Hardware-in-the-Loop (HiL) test environment?
Response:

A. Test of the overall system requirements for the vehicle
B. Test of the driving behavior of the chassis
C. Test of the electronic control unit functions for correct behavior
D. Test of the data exchange between the electronic control units

Answer: C

Question: 10

Which three items are all parts of a Hardware-in-the-Loop (HiL) test environment?

Response:

A. Test case generator, rest bus simulation, power supply
B. Breakout box, software compiler, real parts
C. Power supply, real-time capable computer, electric error simulation
D. Electric error simulation, signal processing, processor simulation

Answer: C

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